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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/815,873	03/22/2001	Steven G. Roskowski	18602-05889 (P0467USR1)	5394
61520 7590 03/28/2008 APPLE/FENWICK SILICON VALLEY CENTER 801 CALIFORNIA STREET MOUNTAIN VIEW, CA 94041			EXAMINER PEYTON, TAMMARA R	
			ART UNIT 2182	PAPER NUMBER
			MAIL DATE 03/28/2008	DELIVERY MODE PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## **SUPPLEMENTAL EXAMINER'S AMENDMENT**

### ***Drawings***

The drawings were received on 1/30/08. These drawings are approved.

1. An supplemental examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
2. The application has been amended as follows:
3. The following changes to the claims 26, 28, and 29 have been approved by Examiner and agreed upon by Applicant's Attorney Sabra-Anne R. Truesdale (Reg. # 55,687) on 1/07/08 and 1/30/08. A follow-up call was placed on 03/19/08 by Applicant's Attorney Sabra-Anne R. Truesdale and the following changes to claim 26 are approved by Examiner.

Replace the Claim 26 filed on 3/3/08 with the following:

**Claim 26 (Currently Amended)**

A method of transferring data between a first component and a second component in a computer system, the first component clocked by a first clock signal, and the second component clocked by a second clock signal, comprising:

clocking data from the first component to a buffer via a data path using the first clock signal in the absence of a valid signal from the [first] second component, the first component always using the first clock signal to transfer data from the first component to the buffer without synchronizing the transfer of the data to another clock

clocking data from the buffer to the second component via the data path using the second clock signal in response to the valid signal from the second component without transferring other data to the buffer; and

supplying the first and second clock signals to the buffer via a multiplexer having a plurality of inputs and an output coupled to the buffer, the inputs for receiving the first and second clock signals and [[a]] the valid signal from the second component, the multiplexer supplying the second clock signal to the buffer in response to the valid signal; wherein the timing of the first clock signal is independent of the timing of the second clock signal.

According to Applicant's Attorney Sabra-Anne R. Truesdale (Reg. # 55,687) in claim 26 the reference to a "valid signal" occurs four times. The first time, which is in the first element in the body of the claim, recites "a valid signal from the *first* component." The second time, which is in the second element in the body of the claim, recites "the valid signal from the *second* component." The third and fourth times, which are in the third element in the body of the claim, recite "a valid signal from the *second* component" and "the valid signal." The first mention of a valid signal should read "a valid signal from the *second component*," and not "a valid signal from the *first* component." This is clear from the rest of claim 26, which refers to a valid signal from the *second* (not the *first*) component. Also, independent claim 23, which recites similar language to claim 26, recites "a valid signal from the *second* component" instead of "a valid signal from the *first* component" (see the second element in the body of the claim).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tammara Peyton whose telephone number is (571) 272-4157. The examiner can normally be reached between 6:30 - 4:00 from Monday to Thursday, (I am off every first Friday), and 6:30-3:00 every second Friday. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alford Kindred can be reached on (571) 272-4037. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300. Any inquiry of a general nature of relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-2100.

/Tammara R Peyton/  
Primary Examiner, Art Unit 2182  
March 21,2008

Application/Control Number: 09/815,873  
Art Unit: 2182

Page 6

<b>Application Number</b> 	<b>Application/Control No.</b>	<b>Applicant(s)/Patent under Reexamination</b>	
	09/815,873	ROSKOWSKI ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Tammara R. Peyton	2182	